

N and P-Channel Complementary Enhancement Mode MOSFET

Features

- N-Channel**

30V / 4.9A

$R_{DS(ON)}=39m\Omega$ (Typ.) @ $V_{GS}=4.5V$

$R_{DS(ON)}=58m\Omega$ (Typ.) @ $V_{GS}=2.5V$

- P-Channel**

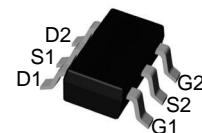
-30V / -3A

$R_{DS(ON)}=82m\Omega$ (Typ.) @ $V_{GS}=-4.5V$

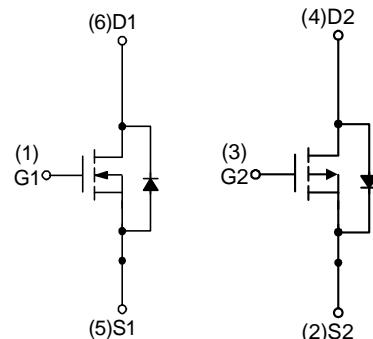
$R_{DS(ON)}=102m\Omega$ (Typ.) @ $V_{GS}=-2.5V$

- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

Pin Description



Top View of TSOP6



N-Channel

P-Channel

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Marking

Marking	F1****
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Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

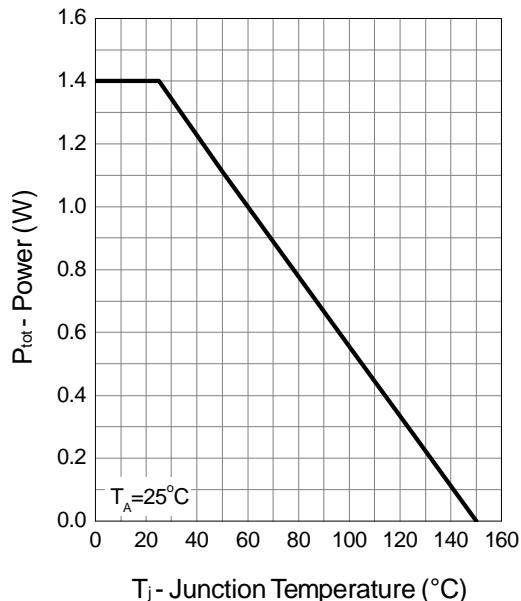
Symbol	Parameter	N Channel	P Channel	Unit
Common Ratings				
V_{DSS}	Drain-Source Voltage	30	-30	V
V_{GSS}	Gate-Source Voltage	± 12	± 12	V
I_D	Continuous Drain Current	$T_A=25^\circ C$	4.9	-3
		$T_A=70^\circ C$	3.9	-2.4
I_{DM}	300μs Pulsed Drain Current	$V_{GS}=10V$	19	-12
I_S	Diode Continuous Forward Current	1		
T_J	Maximum Junction Temperature	150		
T_{STG}	Storage Temperature Range	-55 to 150		
P_D	Maximum Power Dissipation	$T_A=25^\circ C$	1.4	W
		$T_A=70^\circ C$	0.9	
R_{QJA}^*	Thermal Resistance-Junction to Ambient	$t \leq 10s$	90	$^\circ C/W$
		Steady State	125	

N Channel Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

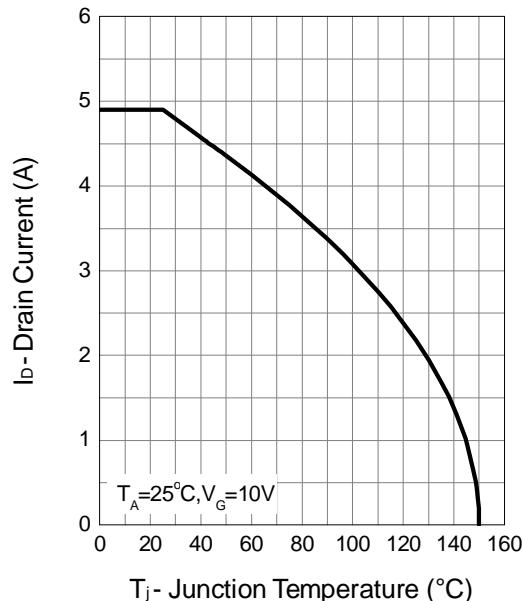
Symbol	Parameter	Test Conditions	N Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu\text{A}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ\text{C}$	-	-	30	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$	0.5	0.7	1.0	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(\text{ON})}^a$	Drain-Source On-State Resistance	$V_{GS}=4.5V, I_{DS}=4.9\text{A}$	-	39	49	$\text{m}\Omega$
		$V_{GS}=2.5V, I_{DS}=3\text{A}$	-	58	68	
Diode Characteristics						
V_{SD}^a	Diode Forward Voltage	$I_{SD}=1\text{A}, V_{GS}=0V$	-	0.75	1.1	V
t_{rr}	Reverse Recovery Time	$I_{SD}=4.9\text{A}, dI_{SD}/dt=100\text{A}/\mu\text{s}$	-	9.2	-	ns
Q_{rr}	Reverse Recovery Charge		-	4.3	-	nC
Dynamic Characteristics ^b						
R_g	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	-	2.3	-	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, \text{Frequency}=1.0\text{MHz}$	-	215	-	pF
C_{oss}	Output Capacitance		-	37	-	
C_{rss}	Reverse Transfer Capacitance		-	28	-	
$t_{d(\text{ON})}$	Turn-on Delay Time	$V_{DD}=15V, R_L=15\Omega, I_{DS}=1\text{A}, V_{GEN}=10V, R_G=6\Omega$	-	5.3	8	ns
T_r	Turn-on Rise Time		-	11	16	
$t_{d(\text{OFF})}$	Turn-off Delay Time		-	12	17	
T_f	Turn-off Fall Time		-	2.6	4	
Gate Charge Characteristics ^b						
Q_g	Total Gate Charge	$V_{DS}=15V, I_{DS}=4.9\text{A}$	$V_{GS}=4.5V$	-	3	nC
			$V_{GS}=10V$	-	5.8	
Q_{gs}	Gate-Source Charge	$V_{DS}=15V, V_{GS}=10V, I_{DS}=4.9\text{A}$	-	1.1	-	nC
Q_{gd}	Gate-Drain Charge		-	1.5	-	
Q_{gth}	Threshold Gate Charge		-	0.5	-	

N Channel Typical Operating Characteristics

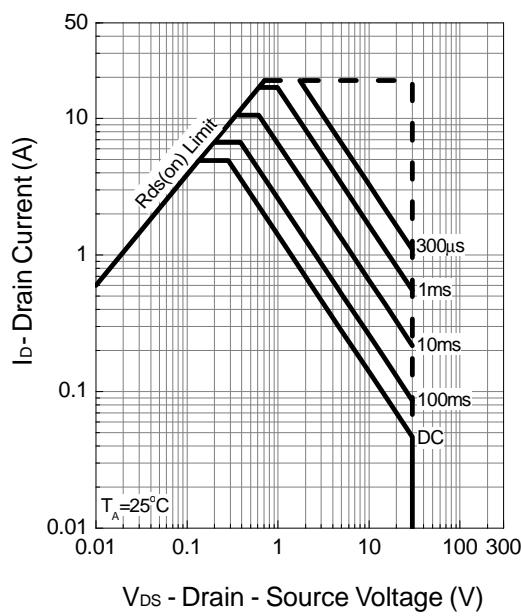
Power Dissipation



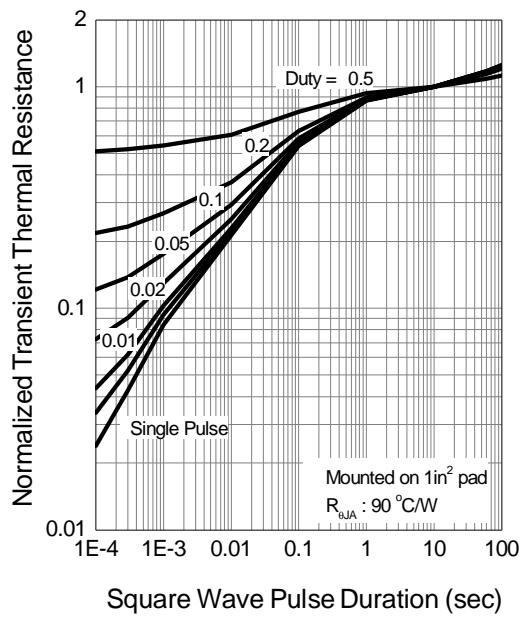
Drain Current



Safe Operation Area

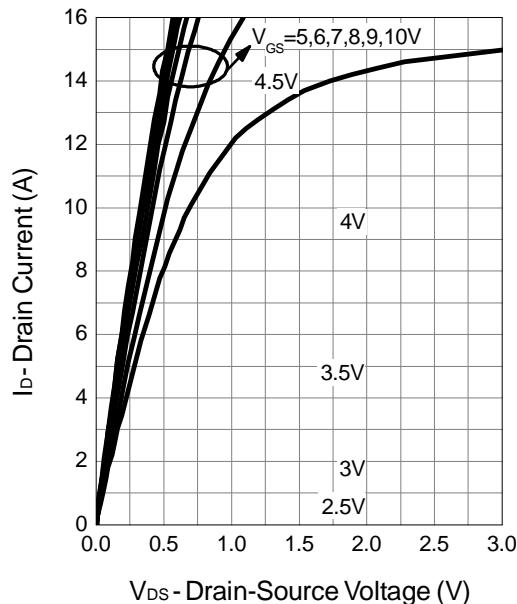


Thermal Transient Impedance

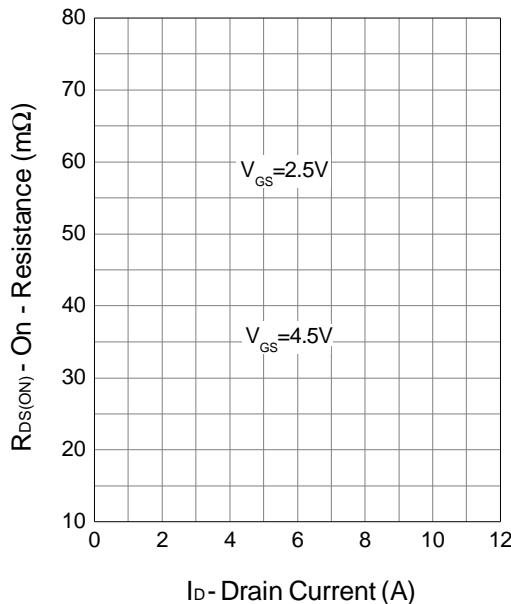


N Channel Typical Operating Characteristics (Cont.)

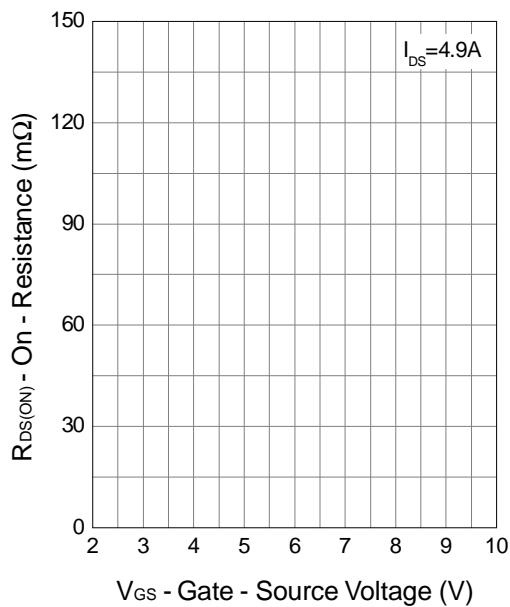
Output Characteristics



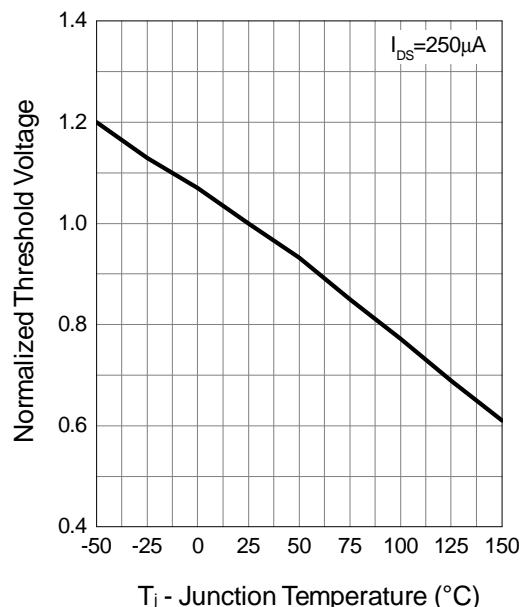
Drain-Source On Resistance



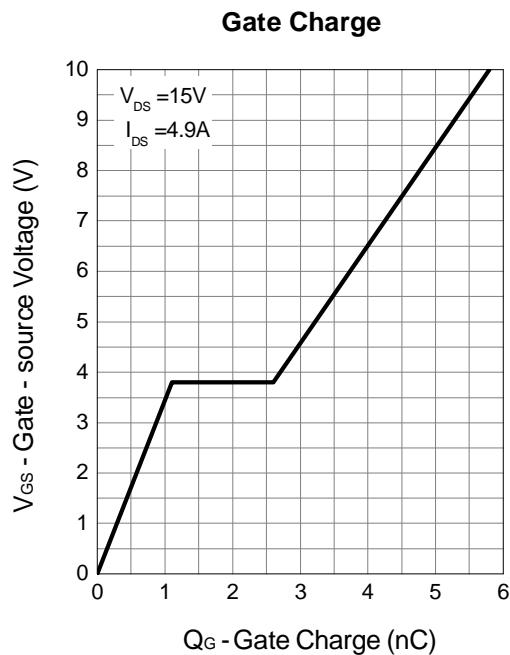
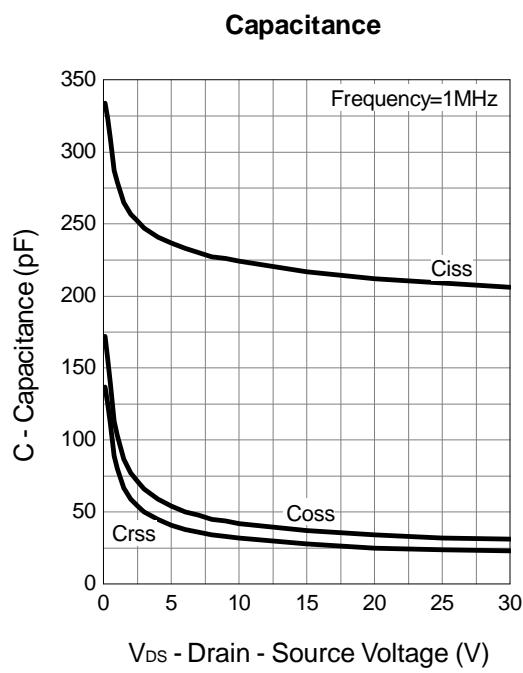
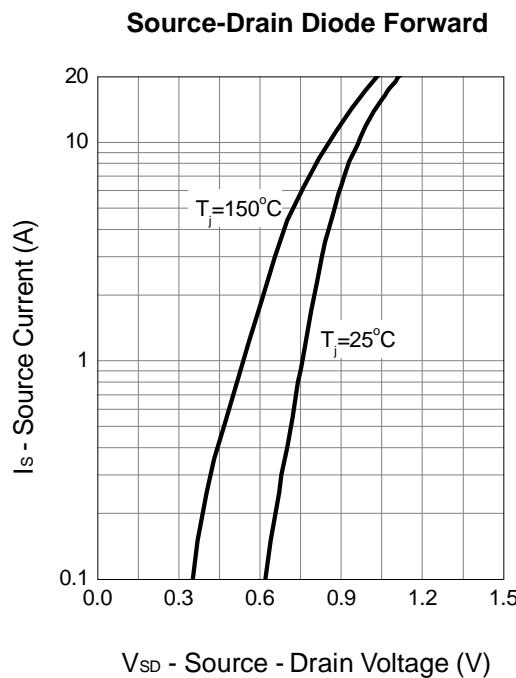
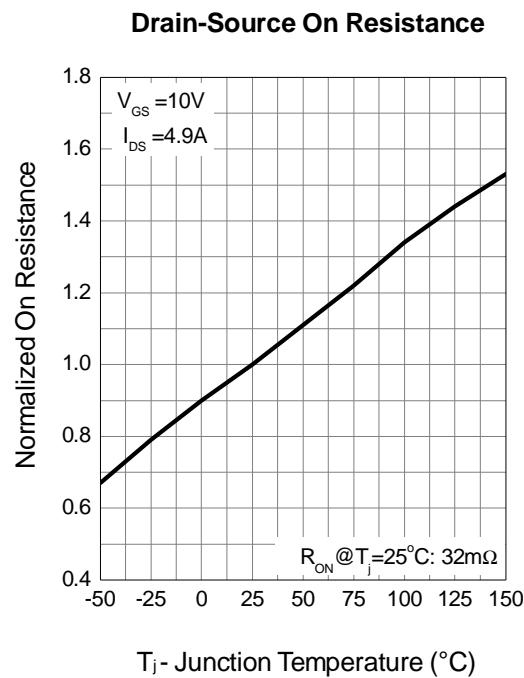
Gate-Source On Resistance



Gate Threshold Voltage



N Channel Typical Operating Characteristics (Cont.)

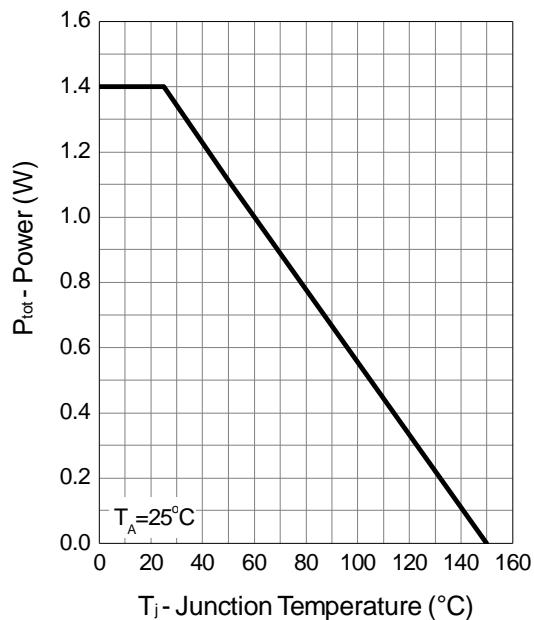


P Channel Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

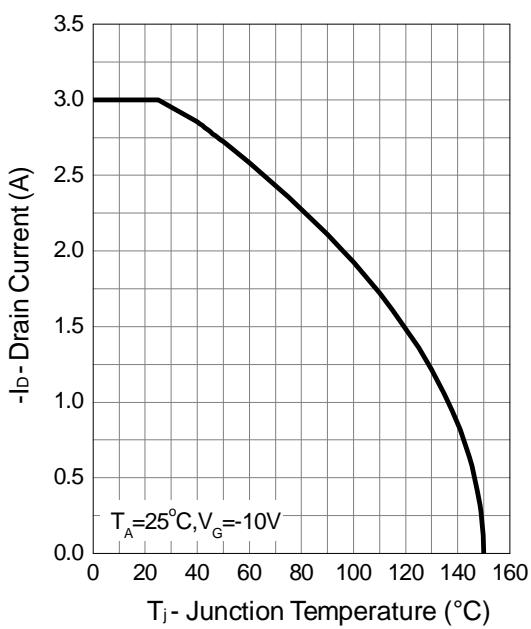
Symbol	Parameter	Test Conditions	P Channel			Unit	
			Min.	Typ.	Max.		
Static Characteristics							
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{DS}}=-250\mu\text{A}$	-30	-	-	V	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA	
		$T_J=85^\circ\text{C}$	-	-	-30		
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=-250\mu\text{A}$	-0.5	-0.7	-1.0	V	
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA	
$R_{\text{DS(ON)}}^{\text{a}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-4.5\text{V}, I_{\text{DS}}=-3\text{A}$	-	82	100	$\text{m}\Omega$	
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{DS}}=-1.9\text{A}$	-	102	150		
Diode Characteristics							
V_{SD}^{a}	Diode Forward Voltage	$I_{\text{SD}}=-1\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.75	-1.1	V	
t_{rr}	Reverse Recovery Time	$I_{\text{SD}}=-3\text{A}, dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	19	-	ns	
Q_{rr}	Reverse Recovery Charge		-	14	-	nC	
Dynamic Characteristics ^b							
R_g	Gate Resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	7	-	Ω	
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-15\text{V}, \text{Frequency}=1.0\text{MHz}$	-	229	-	pF	
C_{oss}	Output Capacitance		-	42	-		
C_{rss}	Reverse Transfer Capacitance		-	33	-		
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{DD}}=-15\text{V}, R_L=15\Omega, I_{\text{DS}}=-1\text{A}, V_{\text{GEN}}=-10\text{V}, R_G=6\Omega$	-	7.2	-	ns	
T_r	Turn-on Rise Time		-	9.3	-		
$t_{\text{d(OFF)}}$	Turn-off Delay Time		-	15.4	-		
T_f	Turn-off Fall Time		-	3.6	-		
Gate Charge Characteristics ^b							
Q_g	Total Gate Charge	$V_{\text{DS}}=-15\text{V}, I_{\text{DS}}=-3\text{A}$	$V_{\text{GS}}=-4.5\text{V},$	-	3.3	-	nC
			$V_{\text{GS}}=-10\text{V}$	-	6.5	-	
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{DS}}=-3\text{A}$	-	1.1	-		
Q_{gd}	Gate-Drain Charge		-	1.1	-		
Q_{gth}	Threshold Gate Charge		-	0.6	-		

P Channel Typical Operating Characteristics

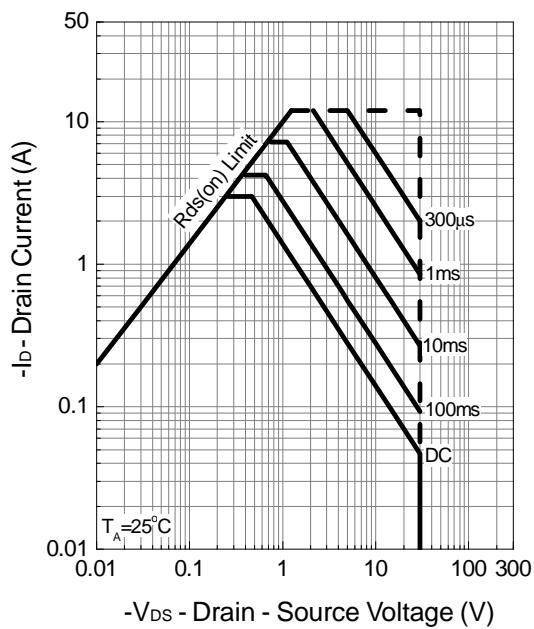
Power Dissipation



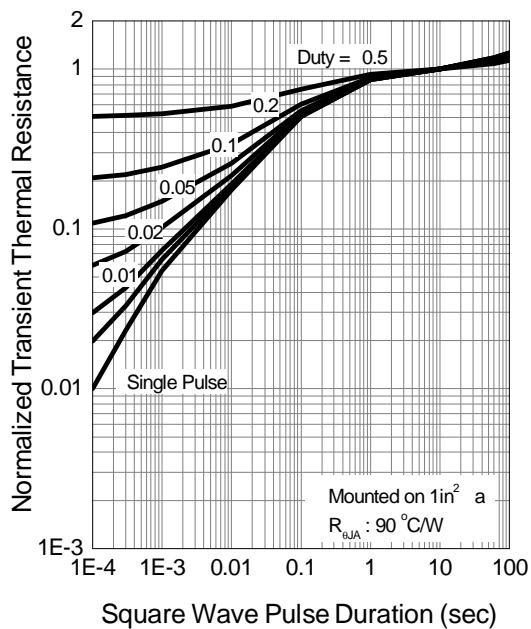
Drain Current



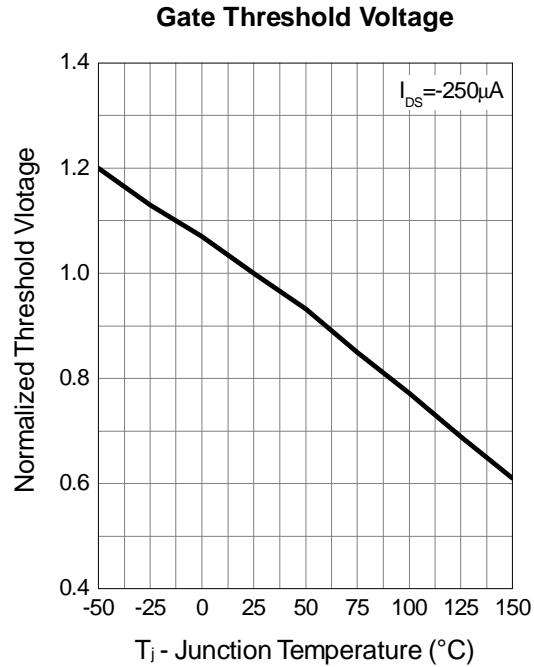
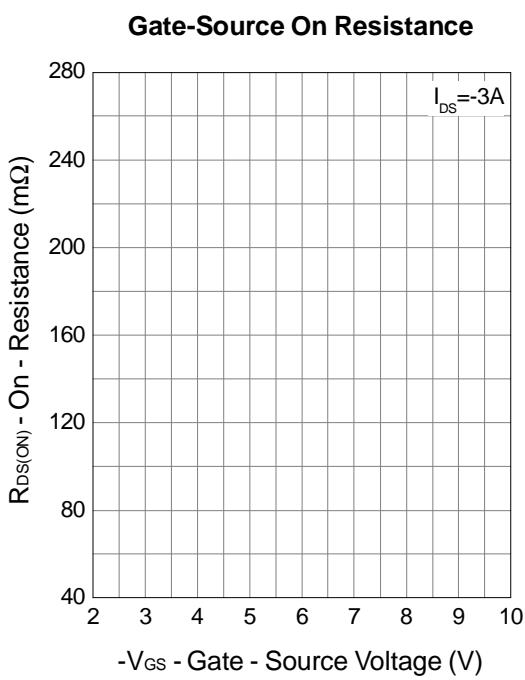
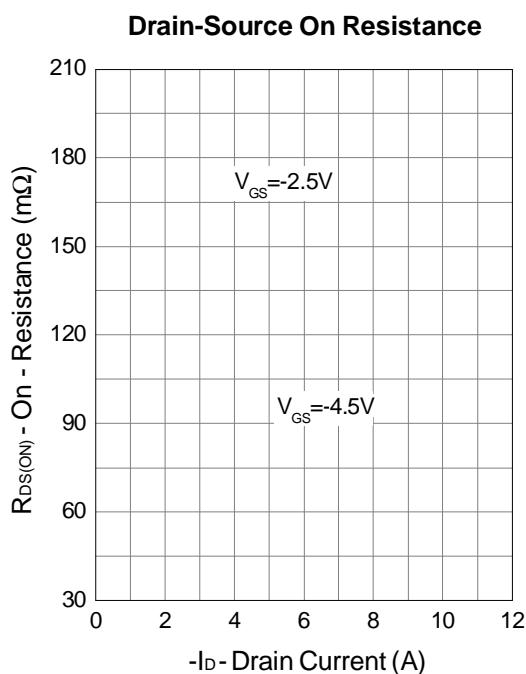
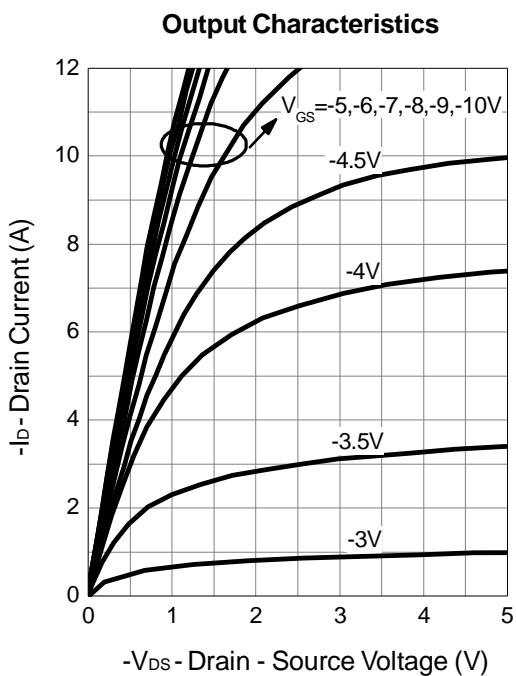
Safe Operation Area



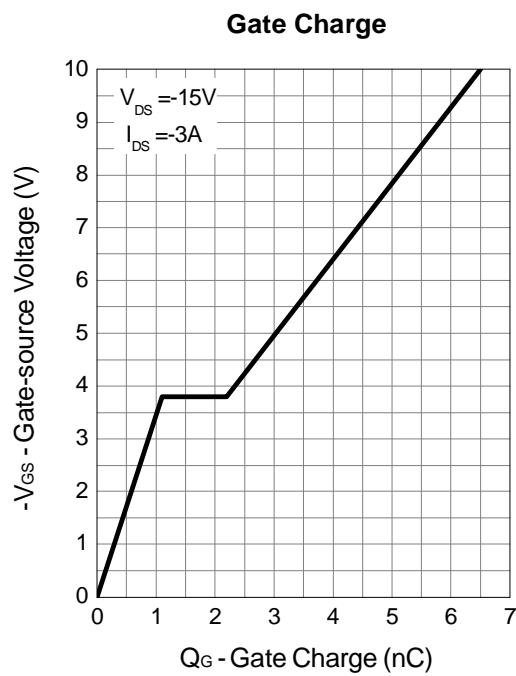
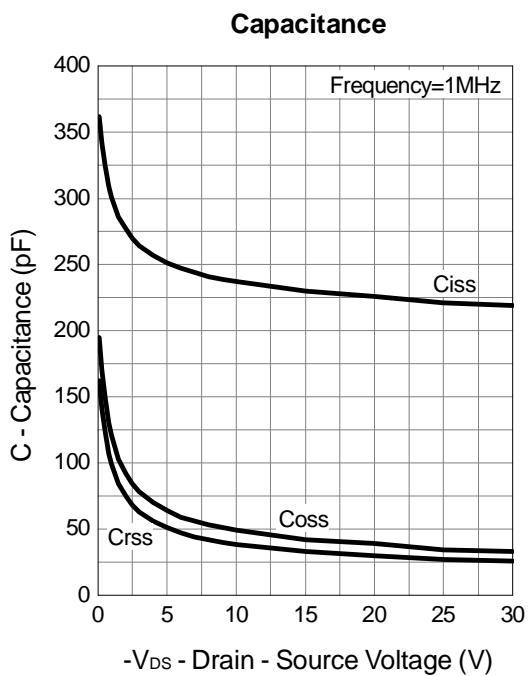
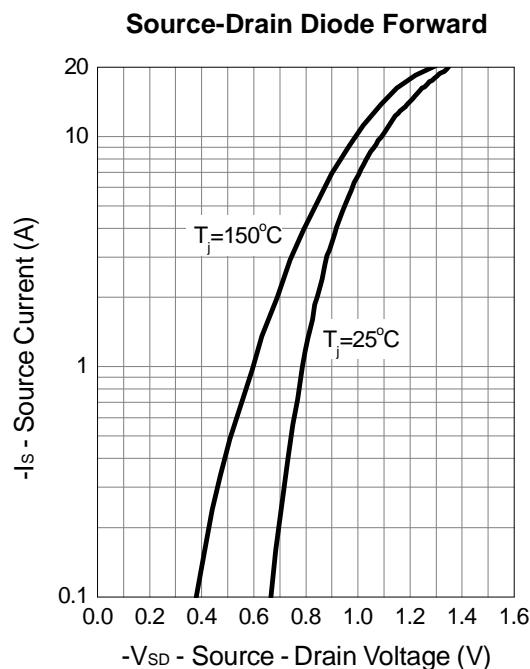
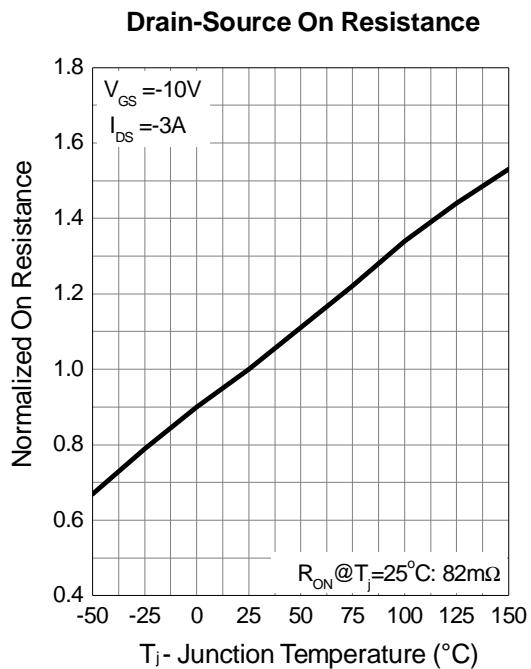
Thermal Transient Impedance



P Channel Typical Operating Characteristics (Cont.)

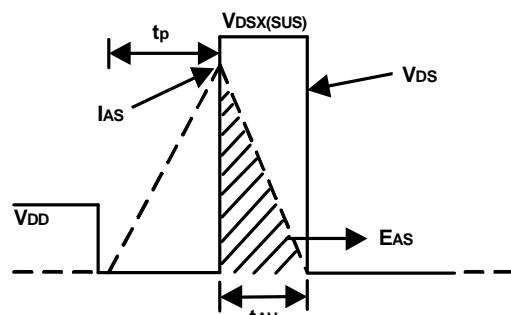
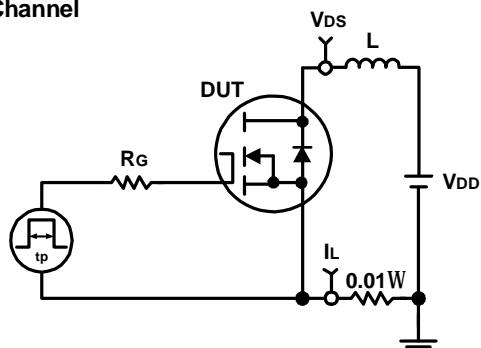


P Channel Typical Operating Characteristics (Cont.)

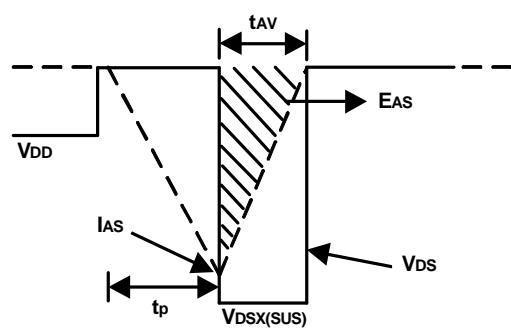
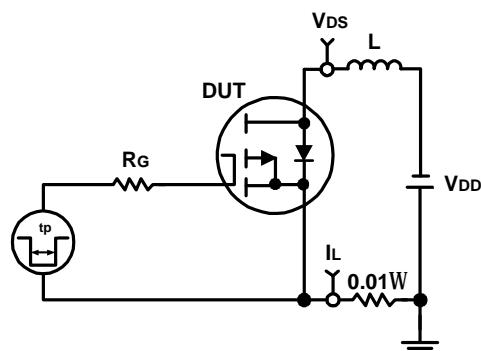


Avalanche Test Circuit and Waveforms

N Channel

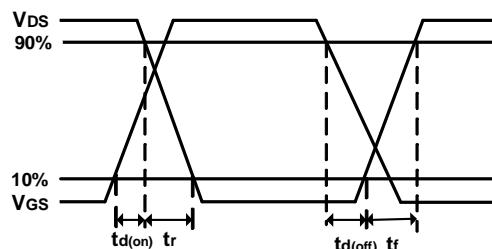
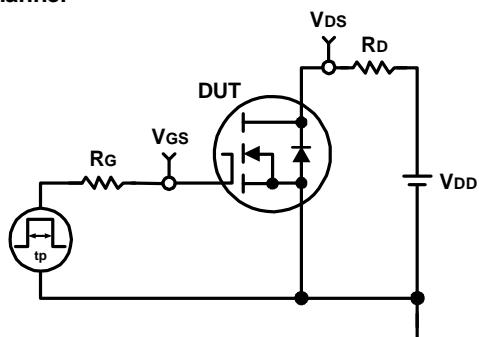


P Channel

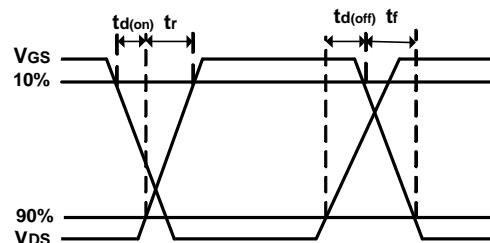
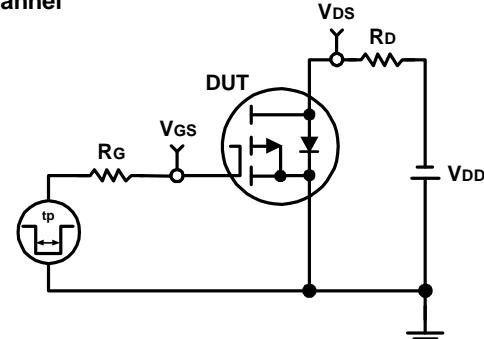


Switching Time Test Circuit and Waveforms

N Channel

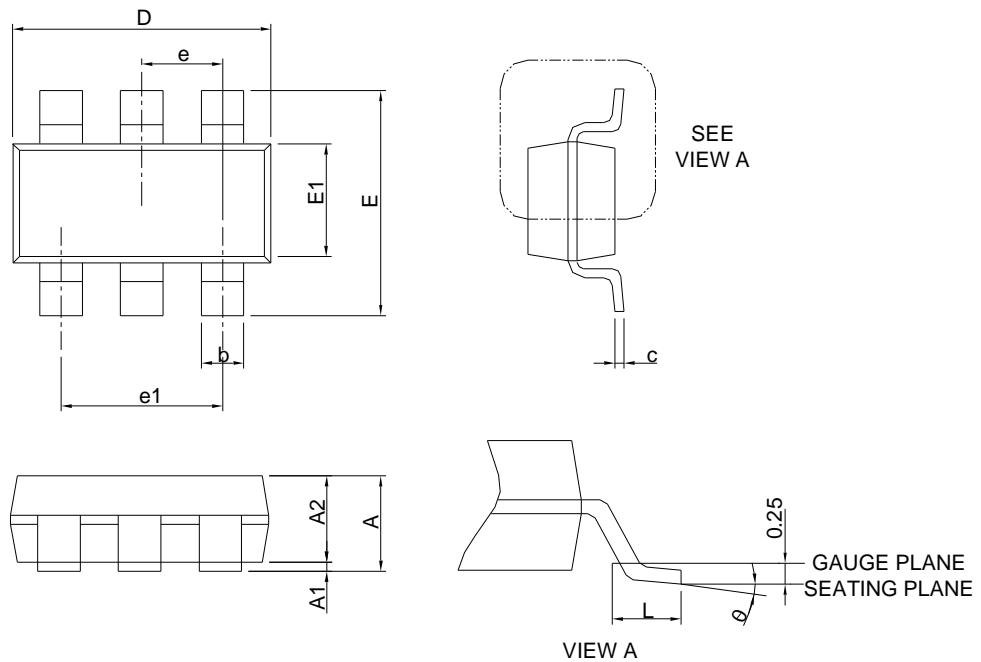


P Channel



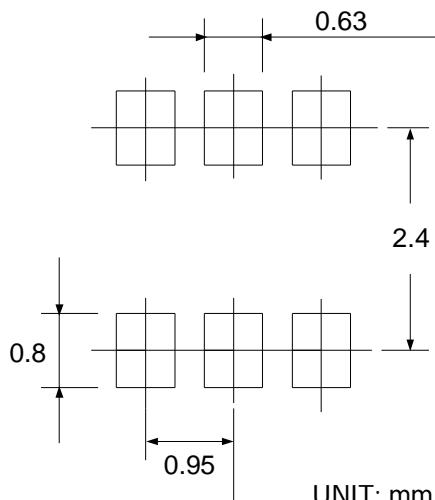
Package Information

TSOP6



S Y M P T O R E M A N G	TSOP6			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	1.25	-	0.049
A1	0.00	0.05	0.000	0.002
A2	0.90	1.20	0.035	0.047
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN



- Note : 1. Follow JEDEC TO-178 AB.
 2. Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.

Attention

- Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress rating only and functional device operation is not implied. YiDeng Wei Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all YiDeng Wei Semiconductor products described or contained herein.
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- YiDeng Wei Semiconductor reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. YiDeng Wei Semiconductor does not assume any liability arising out of the application or use of any product described herein.