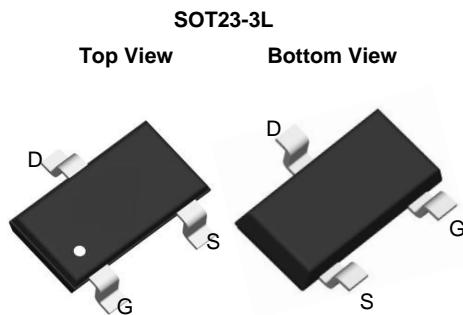


N-Channel Enhancement Mode MOSFET

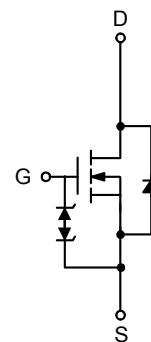
Features

- 20V / 6A
 - $R_{DS(ON)} = 15\text{m}\Omega$ (Typ.) @ $V_{GS} = 10\text{V}$
 - $R_{DS(ON)} = 16\text{m}\Omega$ (Typ.) @ $V_{GS} = 4.5\text{V}$
 - $R_{DS(ON)} = 18\text{m}\Omega$ (Typ.) @ $V_{GS} = 2.5\text{V}$
 - $R_{DS(ON)} = 26\text{m}\Omega$ (Typ.) @ $V_{GS} = 1.8\text{V}$
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)
- ESD Protection



Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems



N-Channel MOSFET

Marking

Marking	AG****
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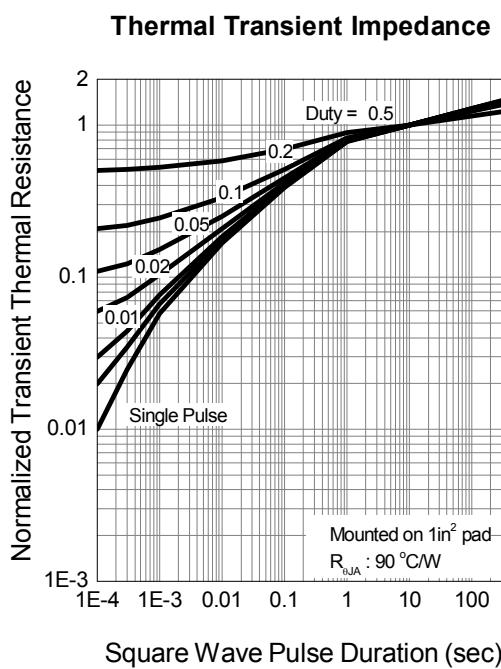
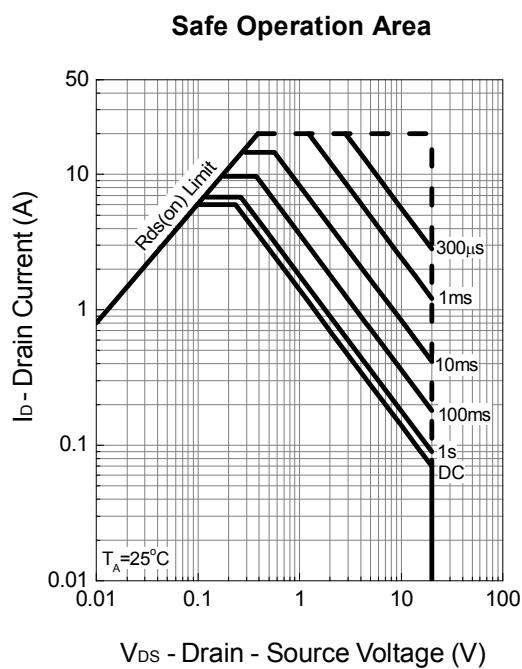
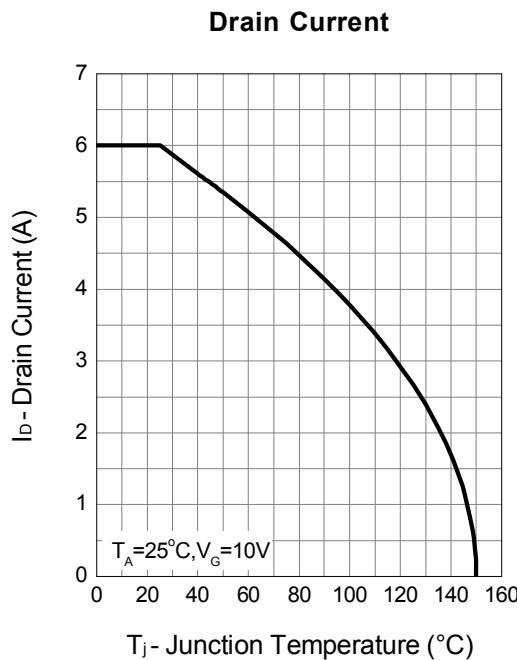
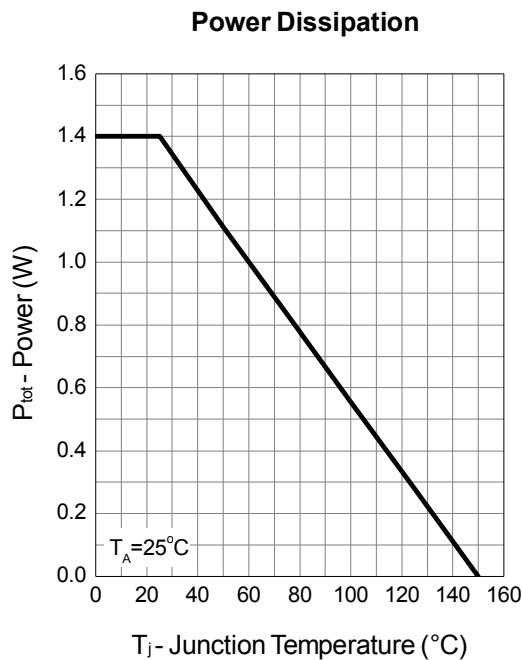
Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Unit
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	± 12	
I_D^*	Continuous Drain Current	$T_A = 25^\circ\text{C}$	A
		$T_A = 70^\circ\text{C}$	
I_{DM}^*	300 μs Pulsed Drain Current	$V_{GS} = 10\text{V}$	20
I_S^*	Diode Continuous Forward Current	1	A
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	
P_D^*	Maximum Power Dissipation	$T_A = 25^\circ\text{C}$	W
		$T_A = 70^\circ\text{C}$	
$R_{\theta JA}^*$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	$^\circ\text{C}/\text{W}$
		Steady State	

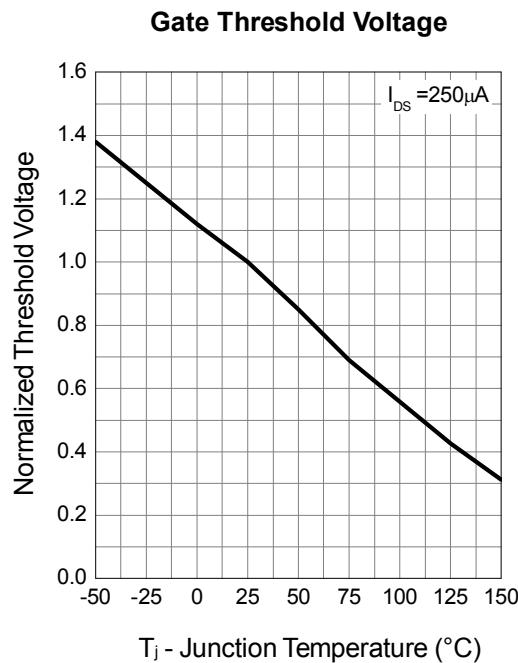
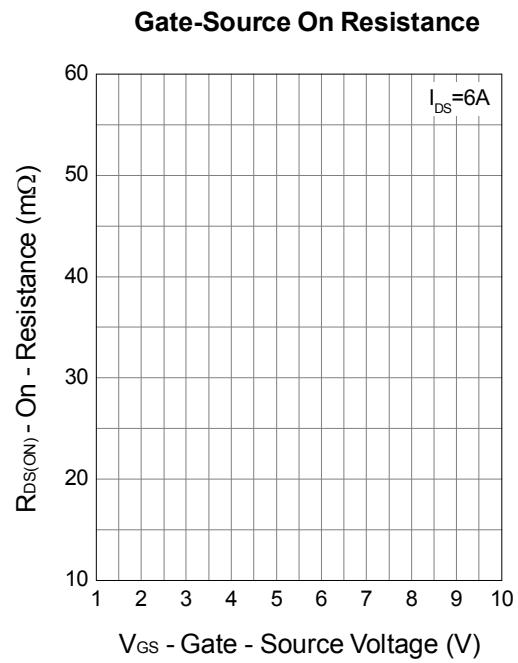
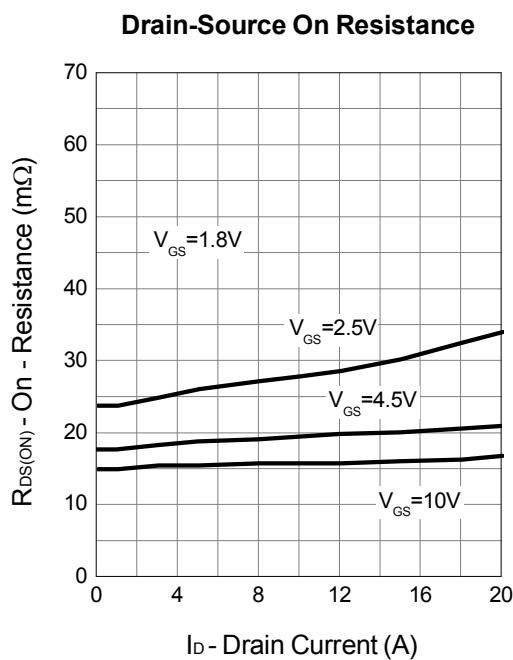
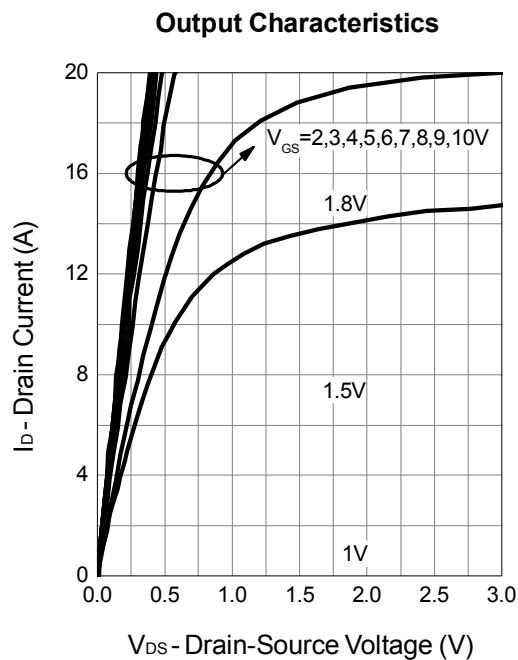
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	XP3416			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{DS}}=250\mu\text{A}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=16\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=85^\circ\text{C}$	-	-	1	μA
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=250\mu\text{A}$	0.5	0.75	1	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 10\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 10	μA
$R_{\text{DS(ON)}}^{\text{a}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{DS}}=6\text{A}$	-	15	20	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{DS}}=6\text{A}$	-	16	22	
		$V_{\text{GS}}=2.5\text{V}, I_{\text{DS}}=2\text{A}$	-	18	26	
		$V_{\text{GS}}=1.8\text{V}, I_{\text{DS}}=1\text{A}$	-	26	34	
V_{SD}^{a}	Diode Forward Voltage	$I_{\text{SD}}=1\text{A}, V_{\text{GS}}=0\text{V}$	-	0.7	1.3	V
Gate Charge Characteristics ^b						
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=4.5\text{V}, I_{\text{DS}}=6\text{A}$	-	8.6	-	nC
Q_{gs}	Gate-Source Charge		-	0.7	-	
Q_{gd}	Gate-Drain Charge		-	3.2	-	
Dynamic Characteristics ^b						
R_G	Gate Resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	5	-	Ω
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=10\text{V}, \text{Frequency}=1.0\text{MHz}$	-	460	-	pF
C_{oss}	Output Capacitance		-	115	-	
C_{rss}	Reverse Transfer Capacitance		-	105	-	
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{DD}}=10\text{V}, R_L=10\Omega, I_{\text{DS}}=1\text{A}, V_{\text{GEN}}=4.5\text{V}, R_G=6\Omega$	-	4	-	ns
t_r	Turn-on Rise Time		-	14	-	
$t_{\text{d(OFF)}}$	Turn-off Delay Time		-	26	-	
t_f	Turn-off Fall Time		-	7.6	-	
t_{rr}	Reverse Recovery Time	$I_{\text{SD}}=6\text{A}, dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	18	-	ns
Q_{rr}	Reverse Recovery Charge		-	5.5	-	nC

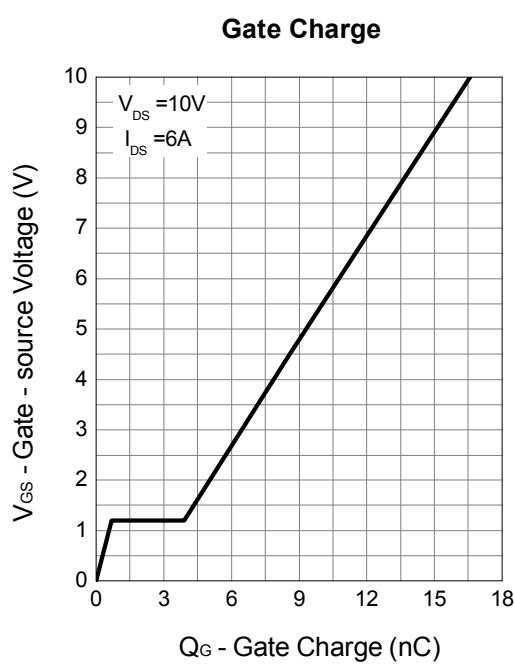
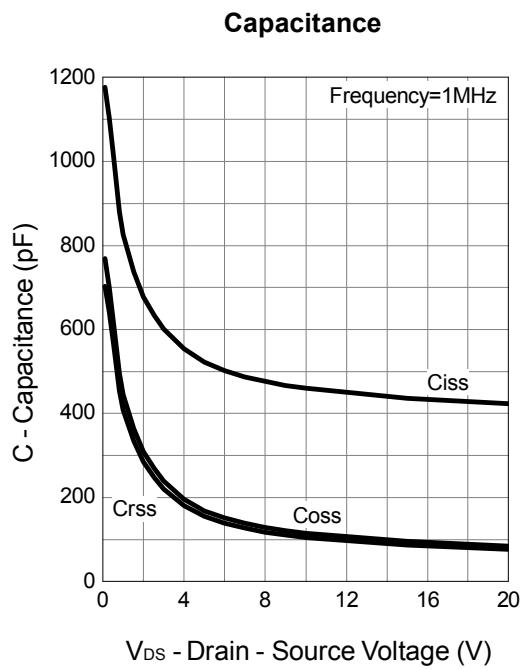
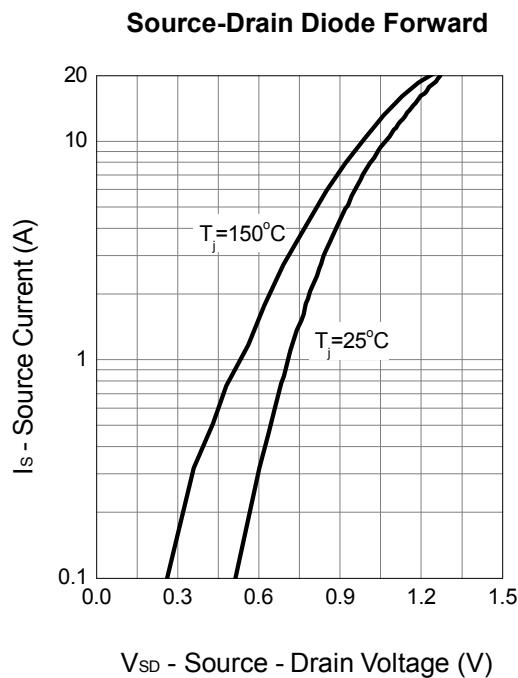
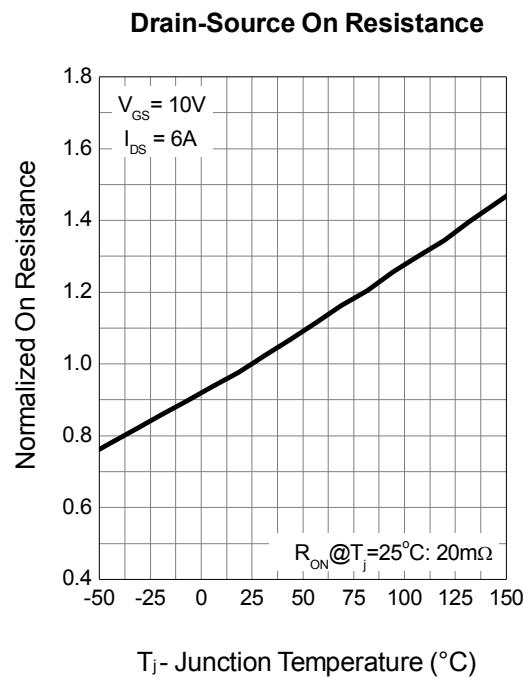
Typical Operating Characteristics



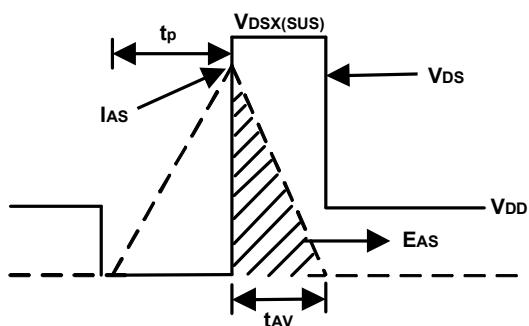
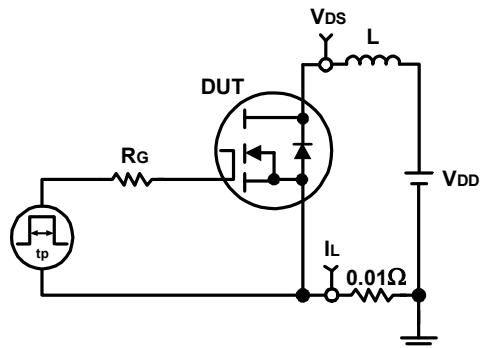
Typical Operating Characteristics (Cont.)



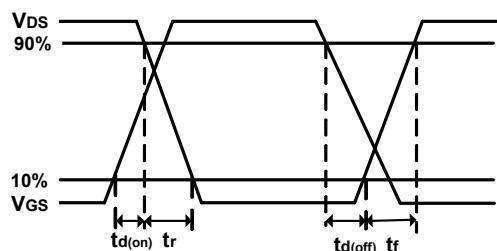
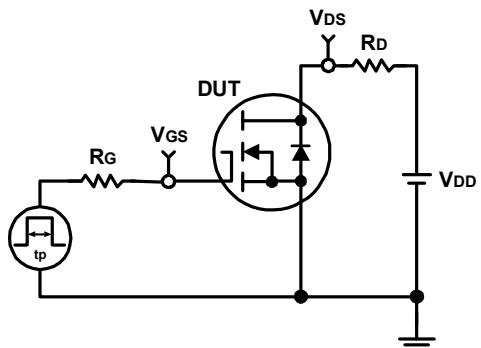
Typical Operating Characteristics (Cont.)



Avalanche Test Circuit and Waveforms

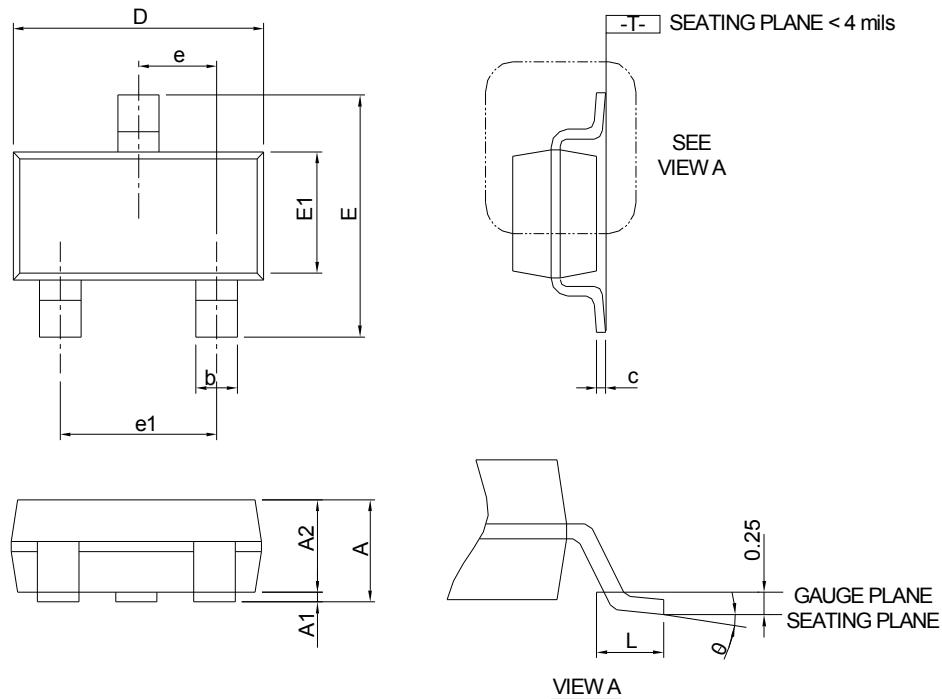


Switching Time Test Circuit and Waveforms



Package Information

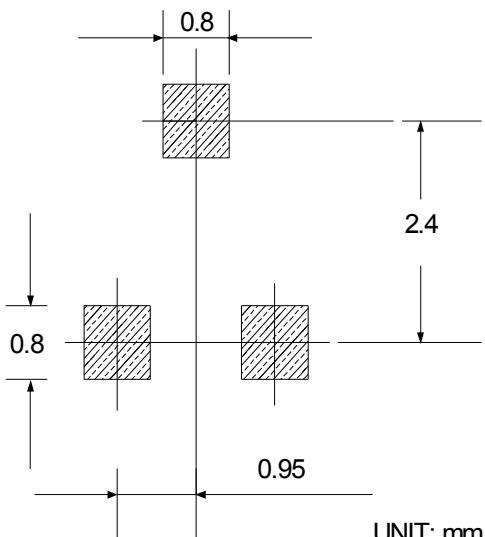
SOT23-3L



SYMBOL	SOT 23-3L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A		1.20		0.047
A1	0.00	0.08	0.000	0.003
A2	0.90	1.12	0.035	0.044
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

Note : Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.

RECOMMENDED LAND PATTERN



UNIT: mm

Attention

- Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress rating only and functional device operation is not implied. YiDeng Wei Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all YiDeng Wei Semiconductor products described or contained herein.
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